

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: Sorin S. Georgescu

Group 2818

Serial No.: 10/600,125

Filed: 6/20/2003

Assignee: Catalyst Semiconductor, Inc.

Examiner: Thinh T. Nguyen

For: NON-VOLATILE MEMORY INTEGRATED CIRCUIT

Mail Stop Issue Fee
 Commissioner for Patents
 P.O. Box 1450
 Alexandria, VA 22313-1450



**TRANSMITTAL OF NEW DRAWING(S) TO CORRECT INFORMALITIES
 WITHIN THREE MONTH PERIOD OF RESPONSE SET
IN NOTICE OF ALLOWABILITY (PTOL 37)**

SUBMISSION OF DRAWING(S)

1. To correct the informalities in the drawings in accordance with the Notice of Allowability dated October 3, 2005, applicant submits herewith new drawings for this application.

Number of sheets of drawings submitted: 8

2. The three month period of response set in the Notice of Allowability (PTOL 37) expires on January 3, 2006. This submission is on or before this expiry date.

Respectfully submitted,



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CERTIFICATE OF MAILING (37 C.F.R. 1.8)

I hereby certify that this correspondence is being deposited with the United States Postal Service as FIRST CLASS MAIL in an envelope addressed to: Mail Stop Issue Fee, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on the date shown below.

10/26/05 Carrie Reddick
 Date Signature: Carrie Reddick